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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Tsuyoshi TOMITA

Art Unit: 2631

Application No.: 09/263,766

Examiner: Burd, Kevin M.

Filed: March 5, 1999

Attorney Dkt. No.: 108075-09006

For: SIGNAL PROCESSING CIRCUIT FOR PREVENTING PSEUDO LOCK OF
TIMING RECOVERY PLL

REPLY UNDER 37 C.F.R. § 1.111

Introductory Comments

Mail Stop: Petitions
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Date: July 8, 2003

Sir:

In reply to the Office Action dated October 16, 2001, together with a Petition for Revival of an Application for Patent Abandoned Unintentionally under 37 C.F.R. §1.137(b) attached hereto, Applicant respectfully submit the following remarks for consideration.

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